Code:3045SEC2

Faculty of Science
B.Sc. III-Year, CBCS-V Semester Examinations, 2018-19
Skill Enhancement Course
PAPER: COMPUTER ORGANIZATION

Time: 2 Hours

Max Marks: 40

Section-A

I. Answer any two of the following questions　　　　　　　　(2x5=10 Marks)

1. Explain register and shift registers.

2. Explain gated SR latch.

3. Differentiate between the circuits designed by ROMs and PLAs.

4. Design a circuit to perform the multiplication of two binary numbers.

Section-B

II. Answer the following questions　　　　　　　　(2x15=30 Marks)

5. (a) Discuss in detail about J-K Flip Flops.

(OR)

(b) Discuss in detail about D- Flip Flops.

6. (a) Explain Decoders with suitable example

(OR)

(b) What is Sequential Circuit? Explain UP/DOWN Counter with an example.

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